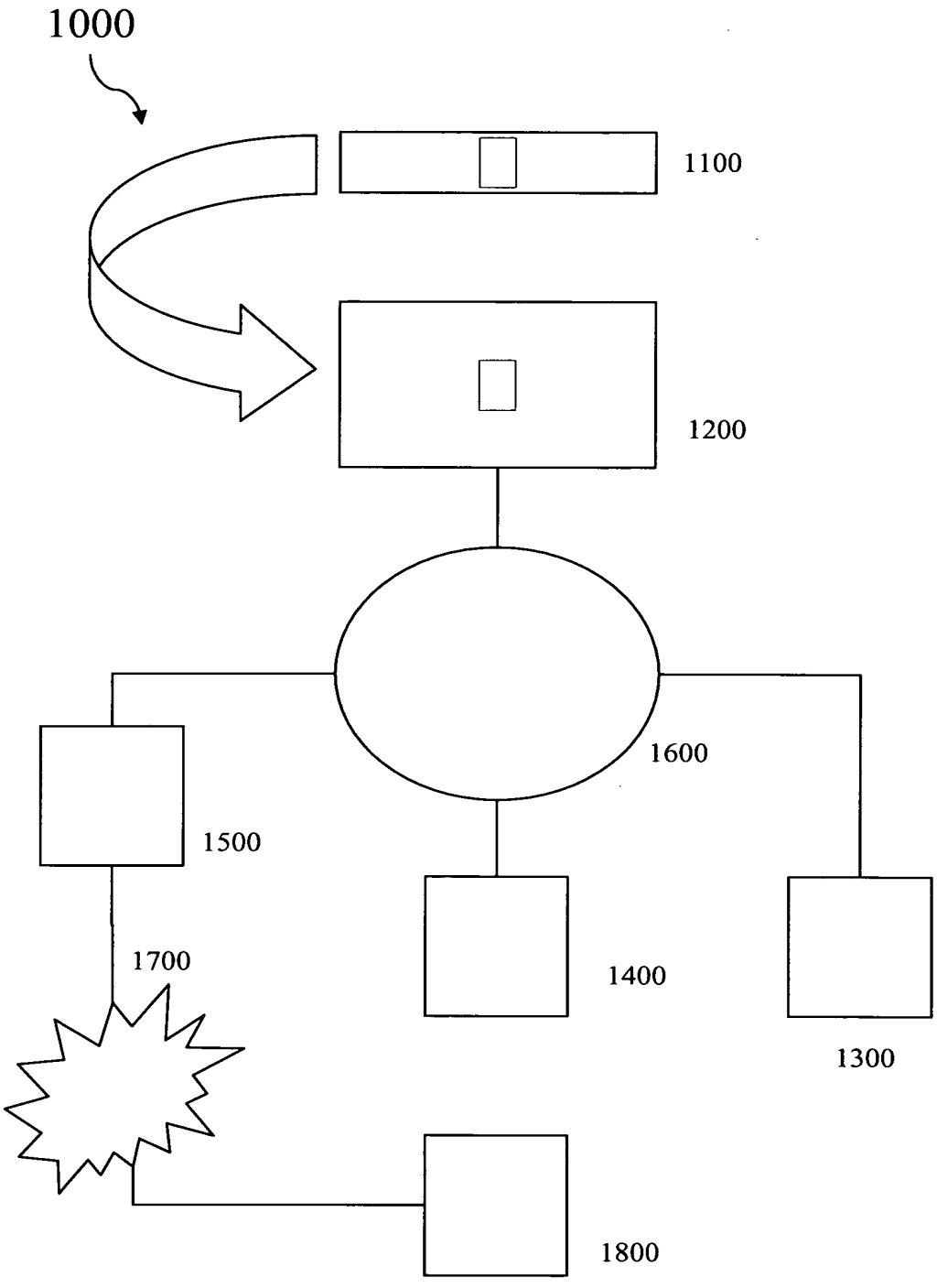

Fig. 1

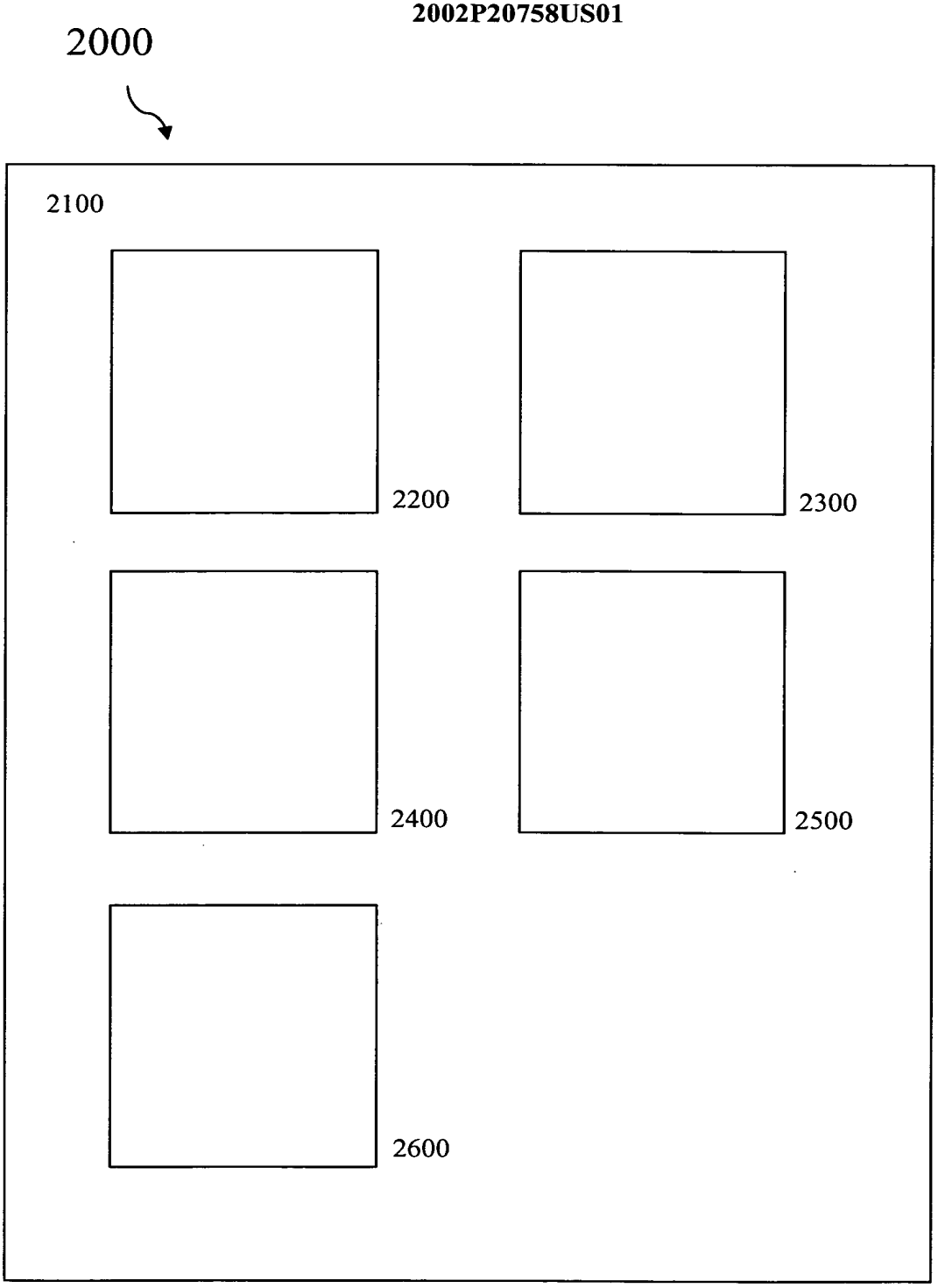

Fig. 2

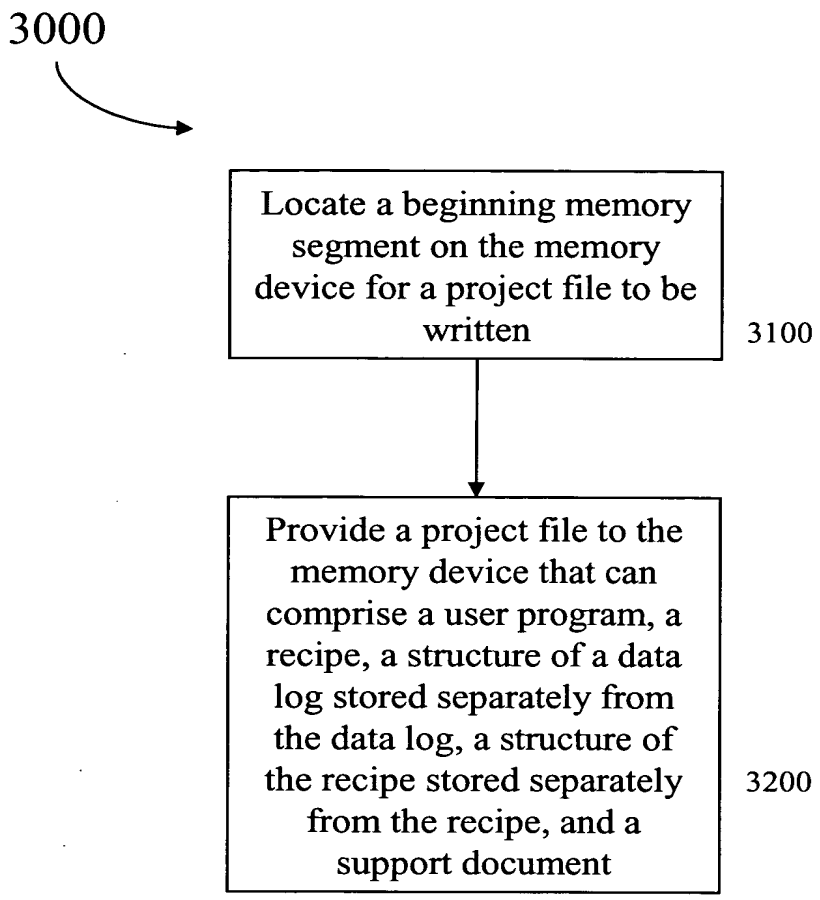
3000
Locate a beginning memory segment on the memory device for a project file to be written
3100
Provide a project file to the memory device that can comprise a user program, a recipe, a structure of a data log stored separately from the data log, a structure of the recipe stored separately from the recipe, and a support document
3200

Fig. 3

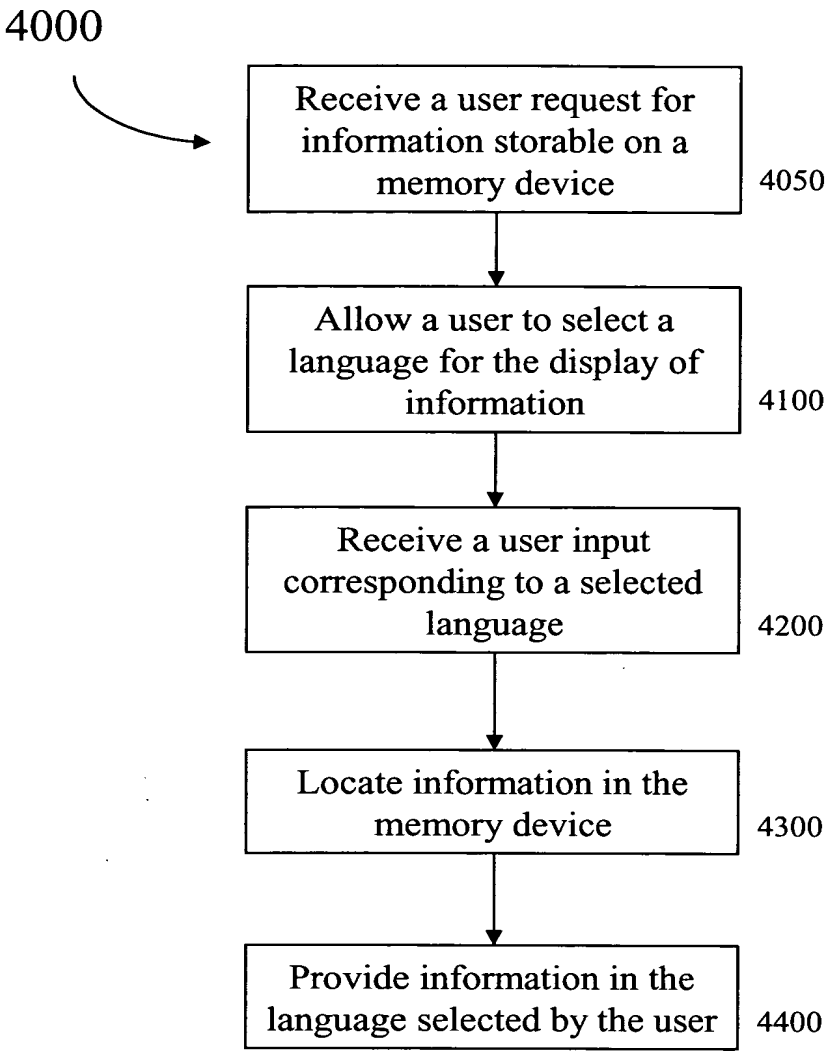
4000
Receive a user request for information storable on a memory device
4050
Allow a user to select a language for the display of information
4100
Receive a user input corresponding to a selected language
4200
Locate information in the memory device
4300
Provide information in the language selected by the user
4400

Fig. 4

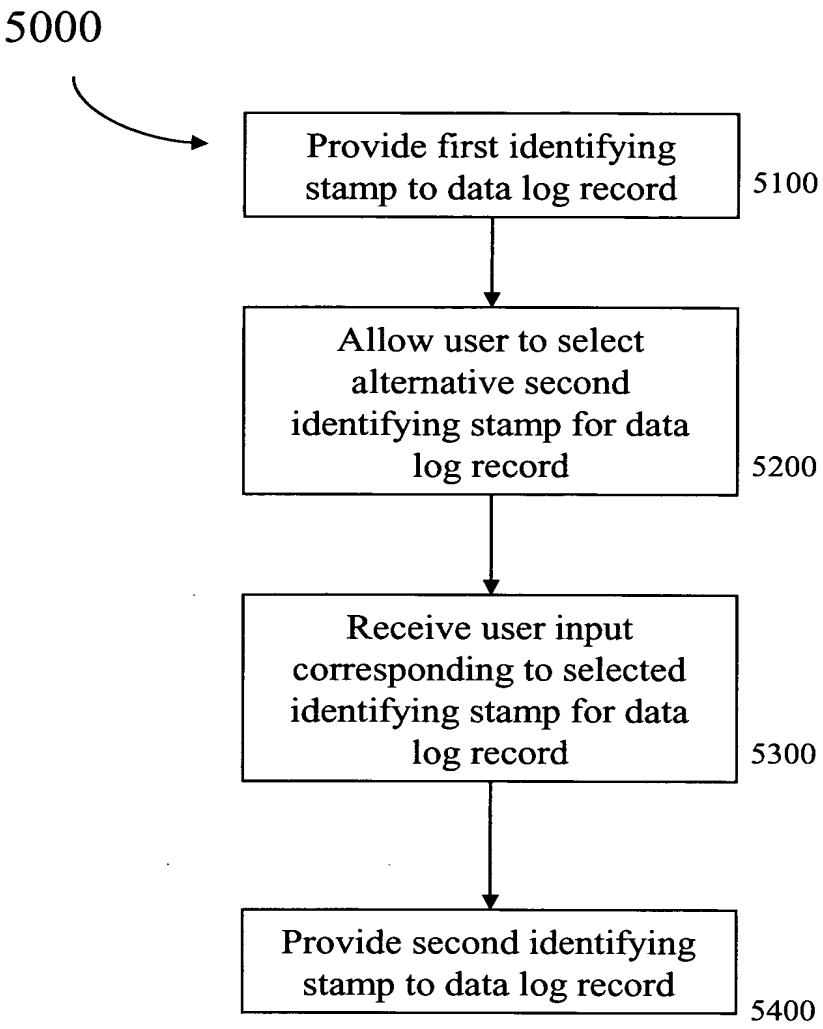
5000
Provide first identifying stamp to data log record
5100
Allow user to select alternative second identifying stamp for data log record
5200
Receive user input corresponding to selected identifying stamp for data log record
5300
Provide second identifying stamp to data log record
5400

Fig. 5

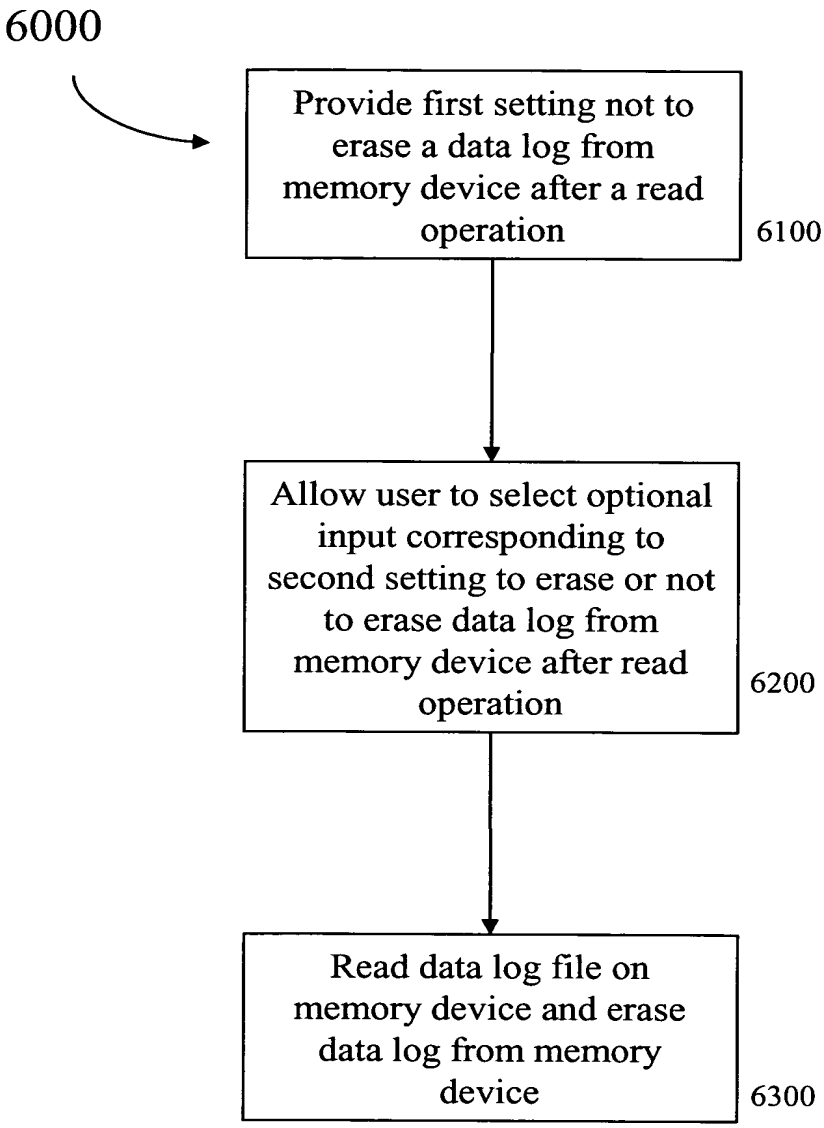
6000
Provide first setting not to erase a data log from memory device after a read operation
6100
Allow user to select optional input corresponding to second setting to erase or not to erase data log from memory device after read operation
6200
Read data log file on memory device and erase data log from memory device
6300

Fig. 6

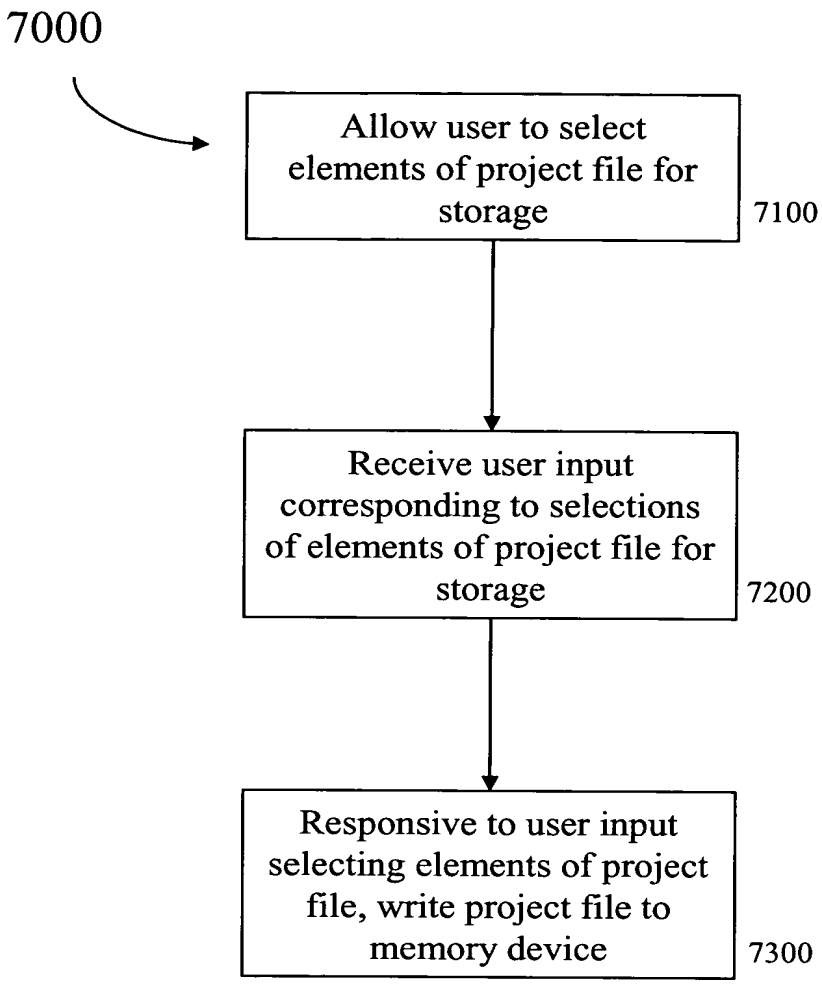
7000
Allow user to select elements of project file for storage
7100
Receive user input corresponding to selections of elements of project file for storage
7200
Responsive to user input selecting elements of project file, write project file to memory device
7300

Fig. 7

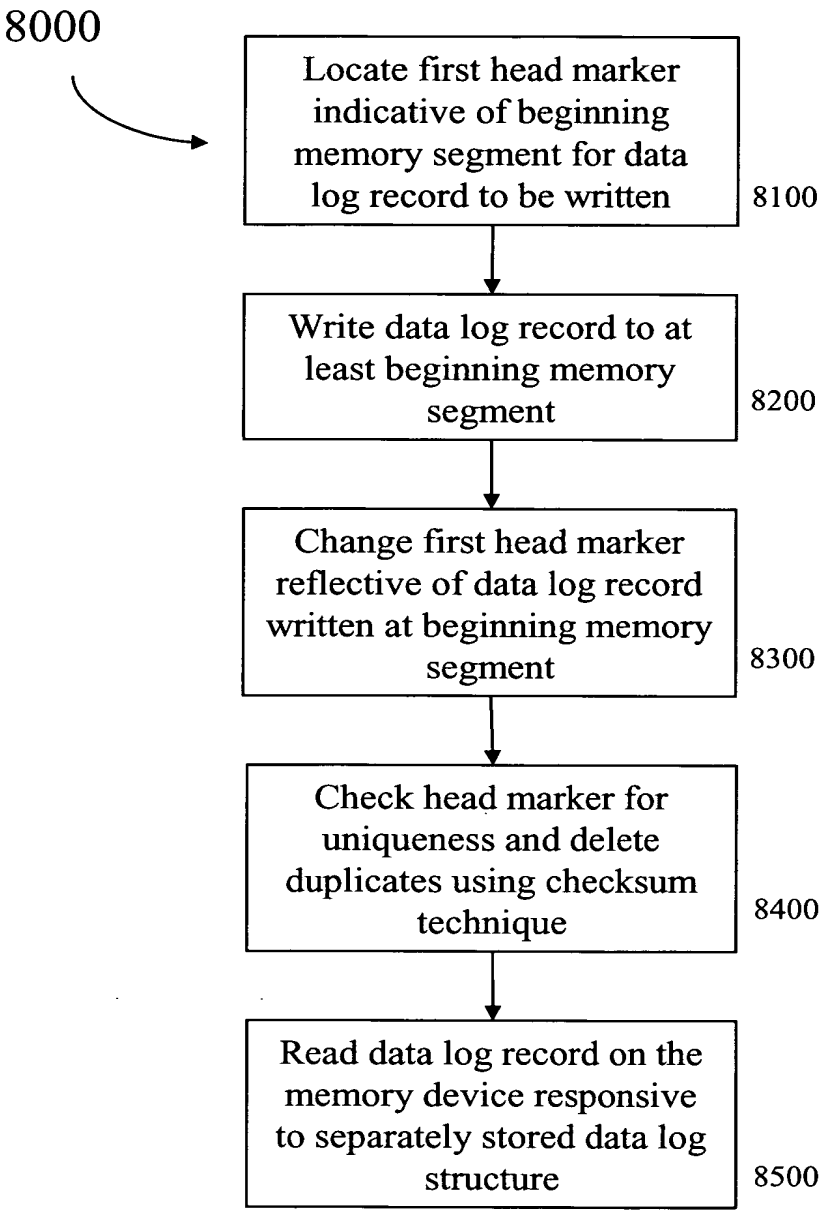
8000
Locate first head marker indicative of beginning memory segment for data log record to be written
8100
Write data log record to at least beginning memory segment
8200
Change first head marker reflective of data log record written at beginning memory segment
8300
Check head marker for uniqueness and delete duplicates using checksum technique
8400
Read data log record on the memory device responsive to separately stored data log structure
8500

Fig. 8

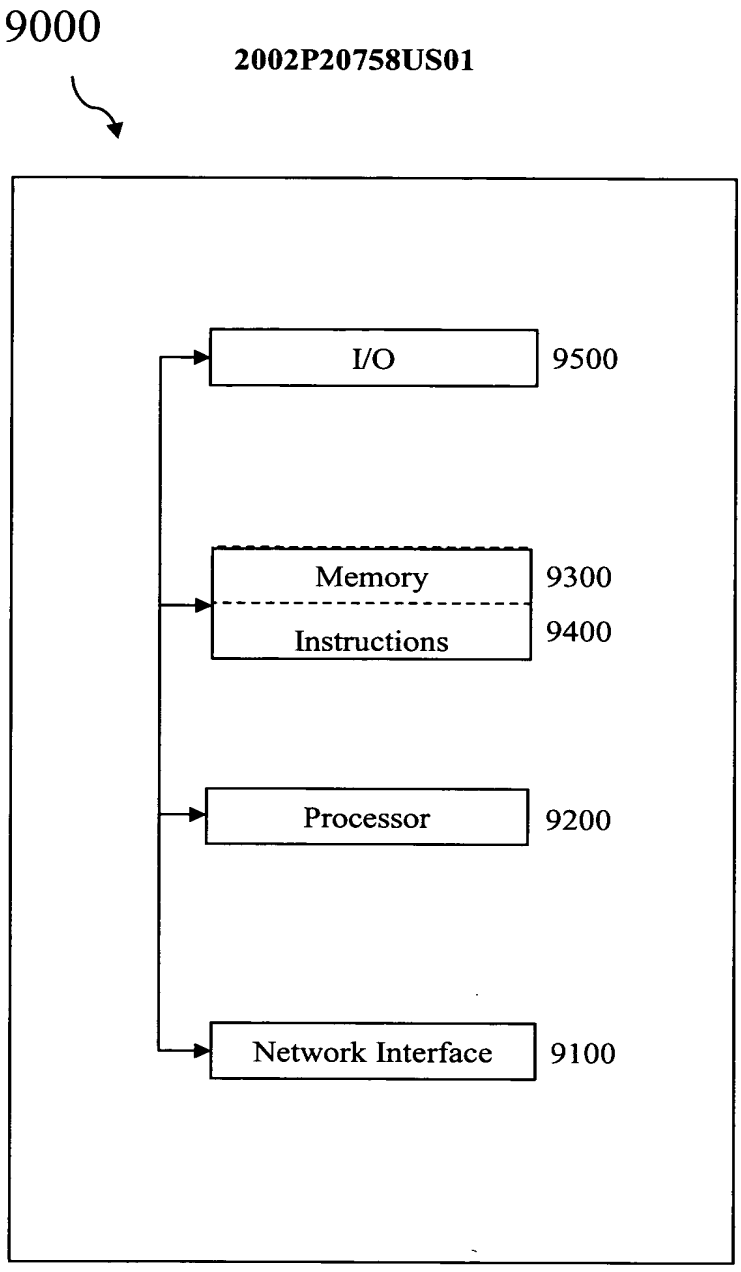
9000
2002P20758US01
I/O
9500
Memory
9300
Instructions
9400
Processor
9200
Network Interface
9100

Fig. 9